

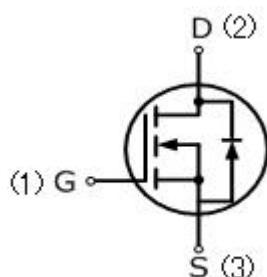
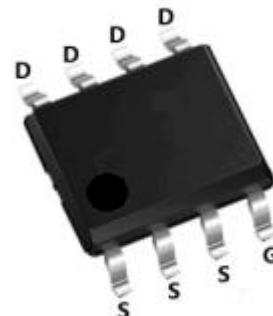


MOSFET

13 Amps, 60 Volts N-CHANNEL MOSFET

FEATURE

- 13A, 60V, $R_{DS(ON)MAX}=10\text{ m}\Omega$ @ $VGS=10\text{ V}/6.5\text{ A}$
 $R_{DS(ON)MAX}=13\text{ m}\Omega$ @ $VGS=4.5\text{ V}/6.5\text{ A}$
- Low gate charge
- Low C_{iss}
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



SOP8L PIN CONFIGURATION

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	13N06P	UNIT
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	
Continuous Drain Current	I_D	13	A
Pulsed Drain Current (Note 1)	I_{DM}	48	
Single Pulse Avalanche Energy (Note 2)	E_{AS}	31	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	260	°C

Thermal Characteristics

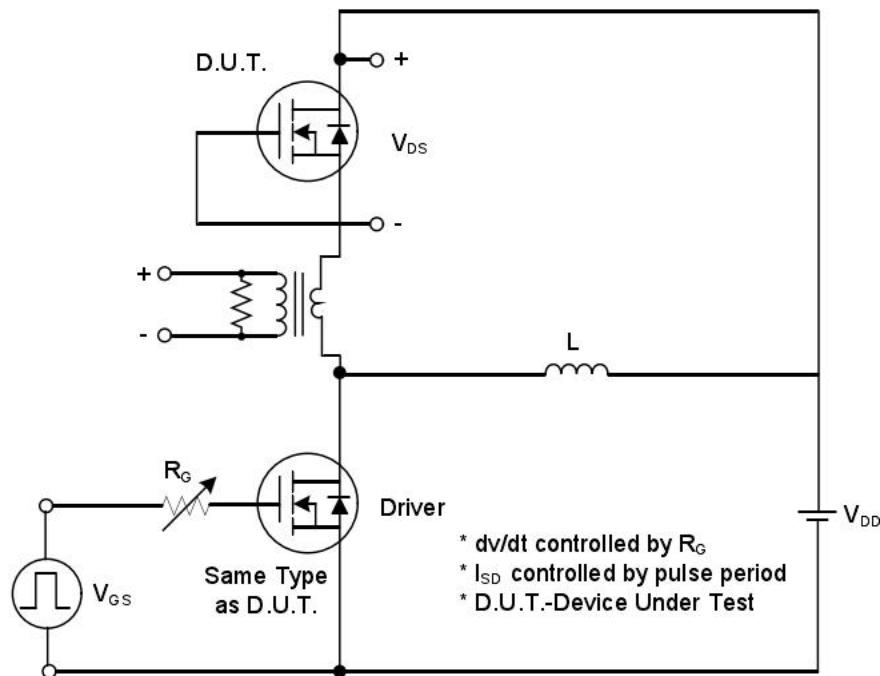
Parameter	Symbol	MAX	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	21.8	°C/W
Maximum Power Dissipation	$T_c=25^\circ\text{C}$	2.8	W

Electrical Characteristics ($T_c=25^\circ\text{C}$,unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	60	—	—	V
Breakdown Temperature Coefficient / ΔT_J	$\Delta \text{BV}_{\text{DSS}}$	Reference to 25°C , $\text{I}_D=250\text{uA}$	—	0.021	—	$\text{V}/^\circ\text{C}$
Zero Gate Voltage Drain Current	$\text{I}_{\text{DS}}^{\text{SS}}$	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	uA
Gate-Body Leakage Current,Forward	I_{GSSF}	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current,Reverse	I_{GSSR}	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
On Characteristics						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	1.4	—	2.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{on})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=6.5\text{A}$	—	9	10	$\text{m } \Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=6.5\text{A}$	—	10.5	13	
Dynamic Characteristics						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHZ}$	950	1062	1150	pF
Output Capacitance	C_{oss}		200	308	400	pF
Reverse Transfer Capacitance	C_{rss}		10	20	30	pF
Switching Characteristics						
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$\text{V}_{\text{DD}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V},$ $\text{R}_G=2.7\Omega$ (Note3,4)	—	8.8	—	ns
Turn-On Rise Time	t_r		—	42	—	ns
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		—	21.5	—	ns
Turn-Off Fall Time	t_f		—	5.4	—	ns
Total Gate Charge	Q_g	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_D=12\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$, (Note3,4)	—	18	—	nC
Gate-Source Charge	Q_{gs}		—	3.7	—	nC
Gate-Drain Charge	Q_{gd}		—	2.9	—	nC
Drain-Source Body Diode Characteristics and Maximum Ratings						
Diode Forward Voltage	V_{SD}	$\text{I}_S=1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1.0	V
Reverse Recovery Time	t_{rr}	$\text{V}_{\text{R}}=30\text{V}, \text{I}_F=12\text{A},$ $d\text{I}_F/dt=300\text{A/us}$	—	78	—	ns
Reverse Recovery Charge	Q_{rr}		—	192	—	nC

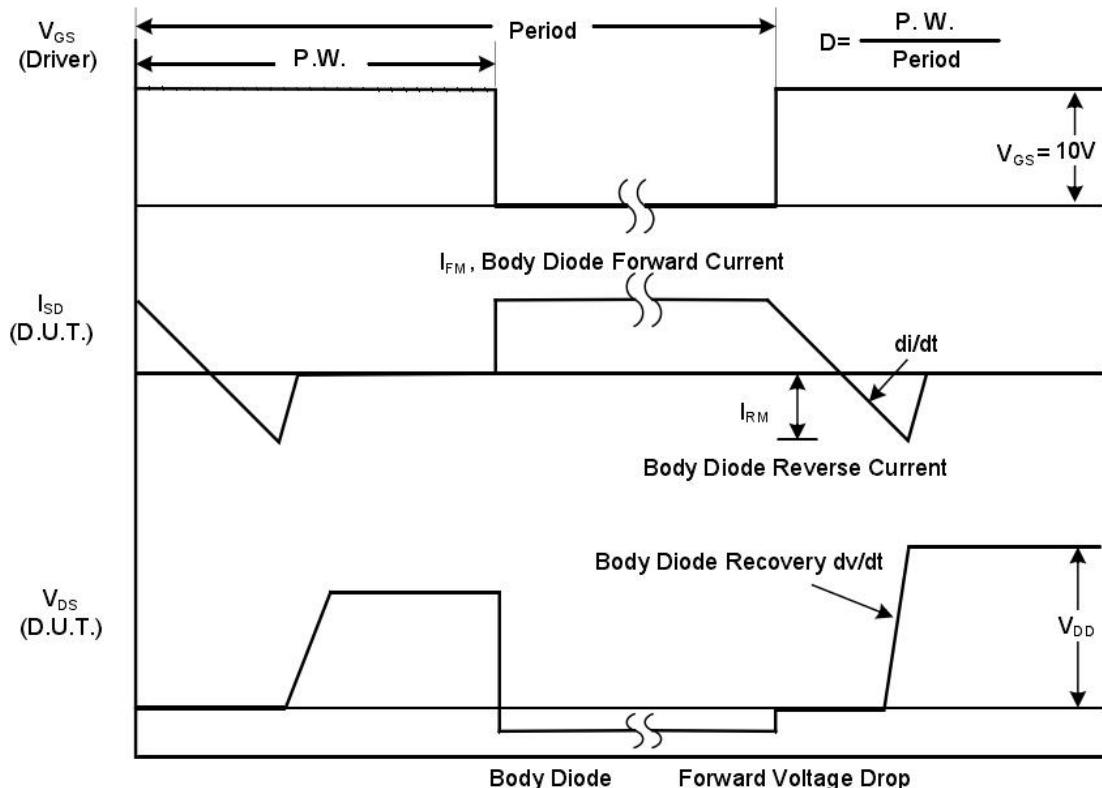
Notes

1. Repetitive Rating:pulse width limited by maximum junction temperature.
2. $\text{V}_{\text{DD}}=30\text{V}, L=0.3\text{mH}, R_g=10 \Omega, I_{AS}=10\text{A}, T_J=25^\circ\text{C}$.
3. $I_{SD} \leq I_D, dI/dt=200\text{A/us}, V_{DD} \leq \text{BV}_{\text{DSS}}$,starting $T_J=25^\circ\text{C}$,Pulse width $\leq 300\text{us}$;duty cycle $\leq 2\%$.
4. Repetitive rating; pulse width limited by maximum junction temperature.

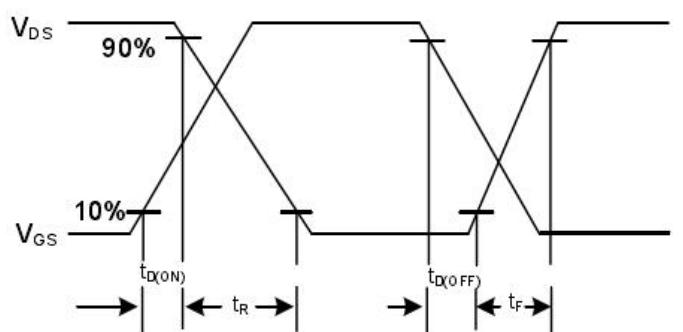
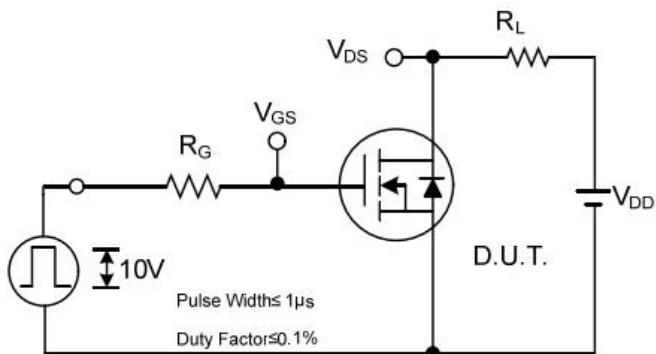
TEST CIRCUIT AND WAVEFORM



Peak Diode Recovery dv/dt Test Circuit

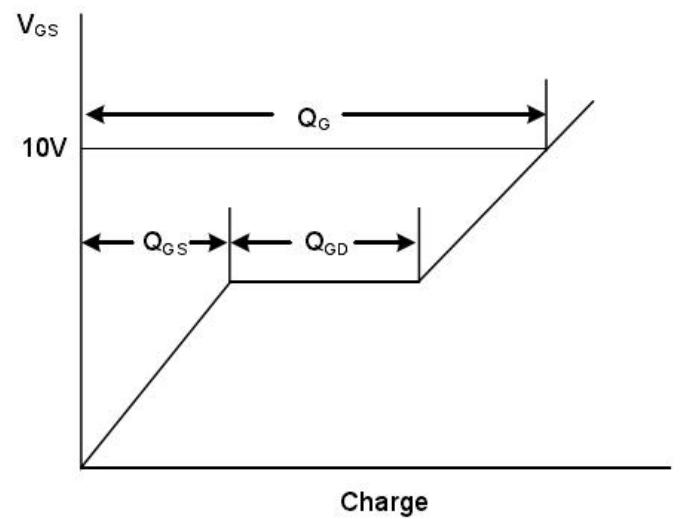
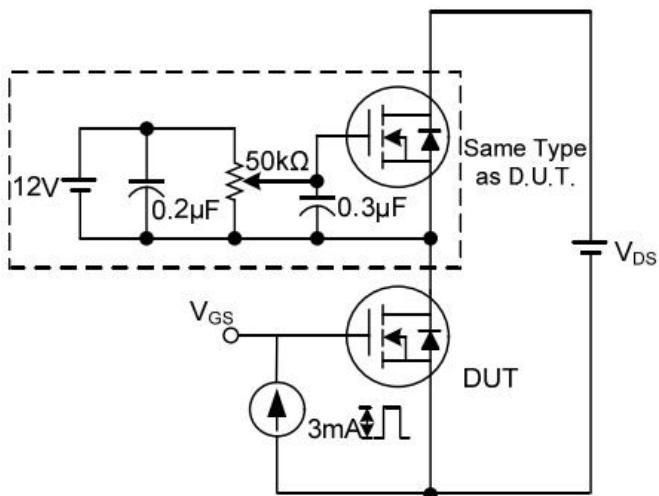


Peak Diode Recovery dv/dt Waveforms



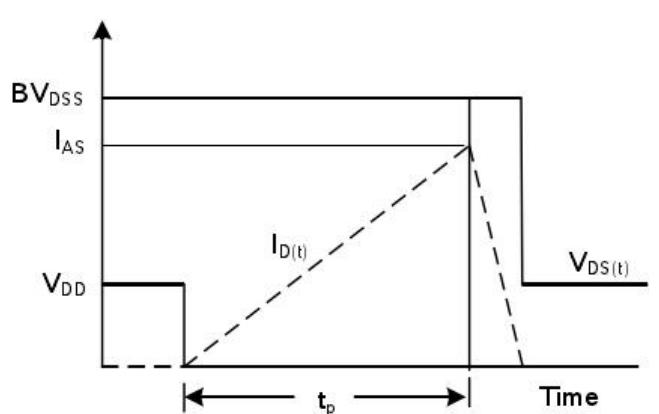
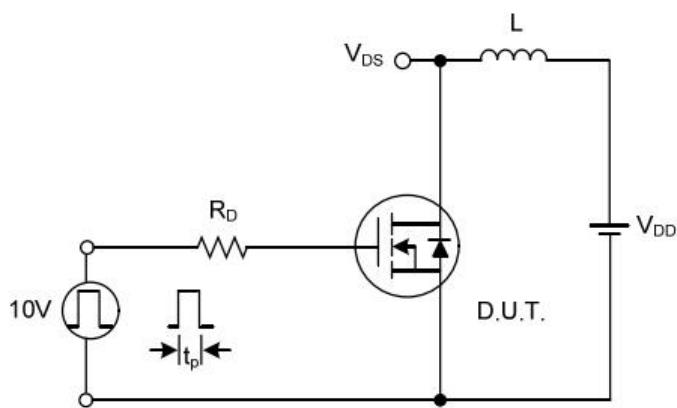
Switching Test Circuit

Switching Waveforms



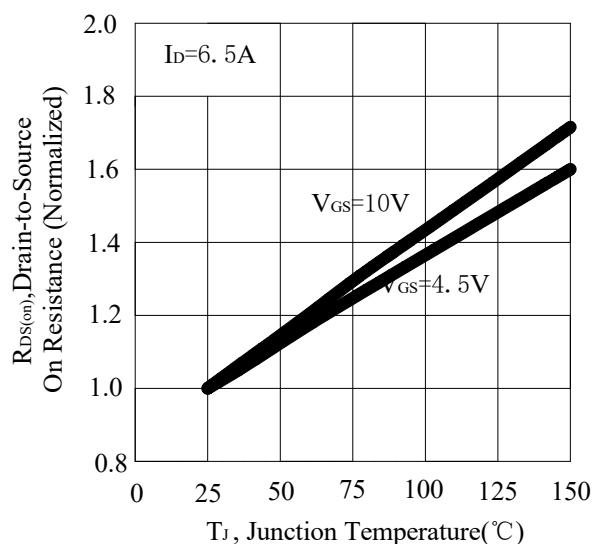
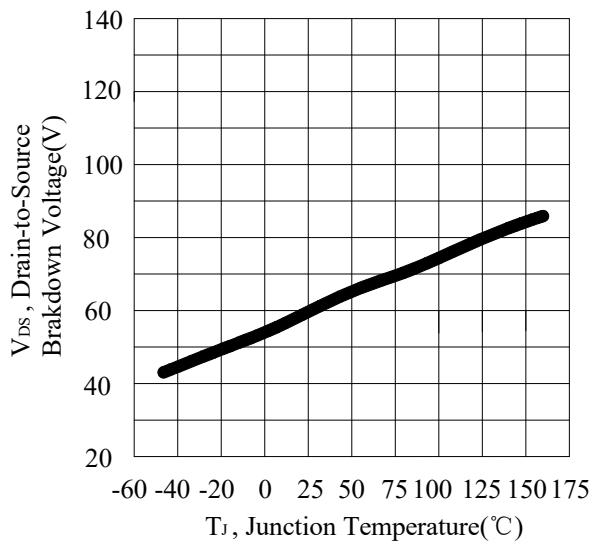
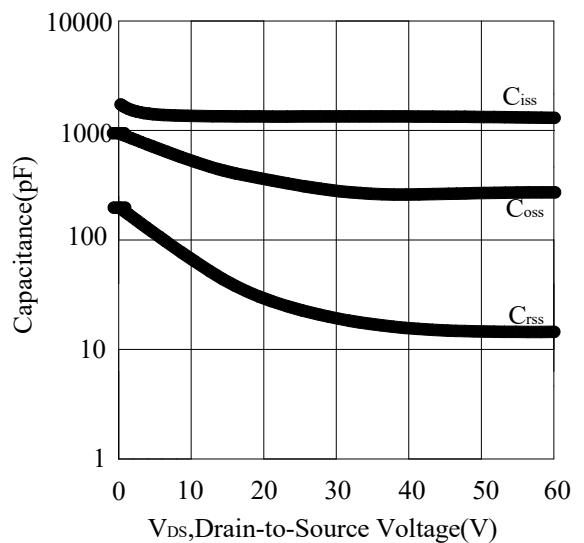
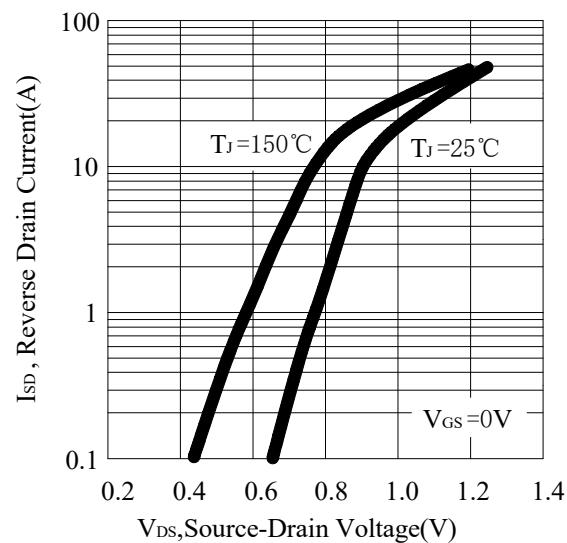
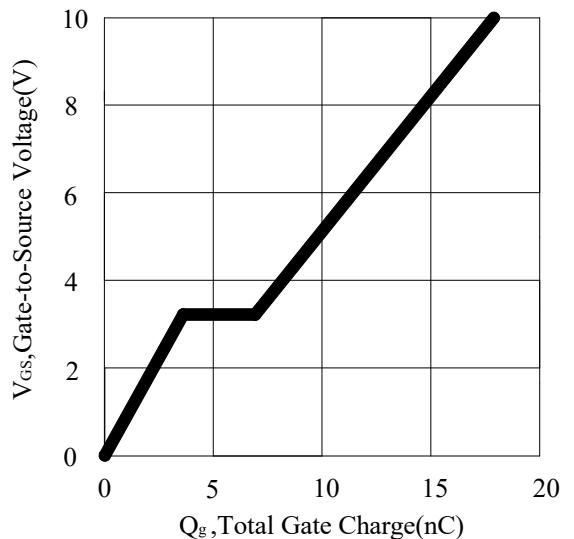
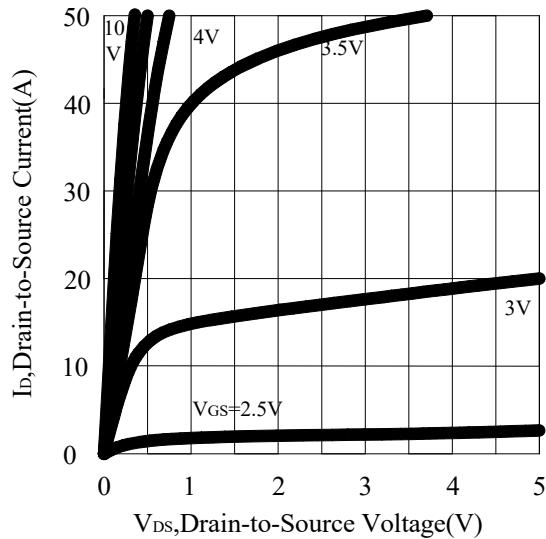
Gate Charge Test Circuit

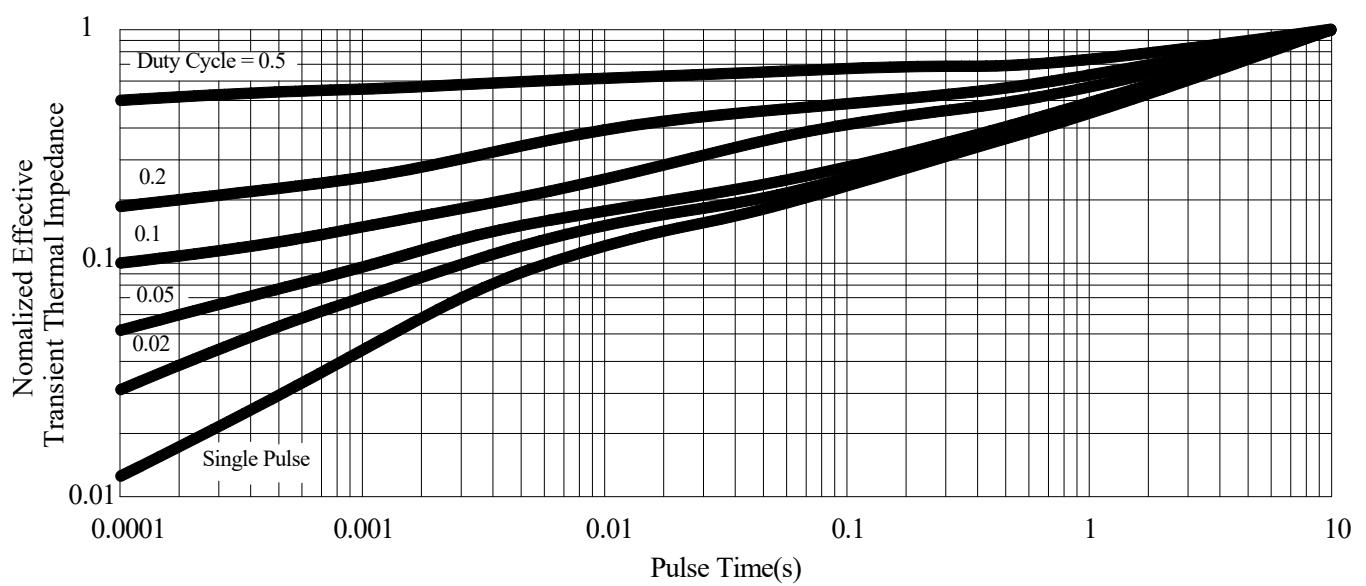
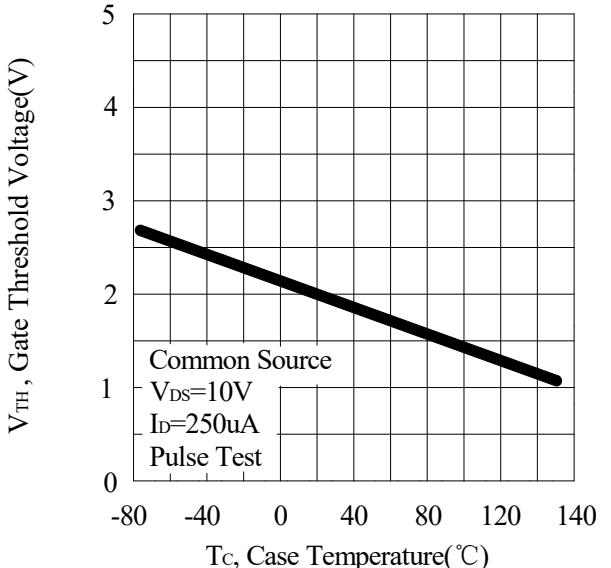
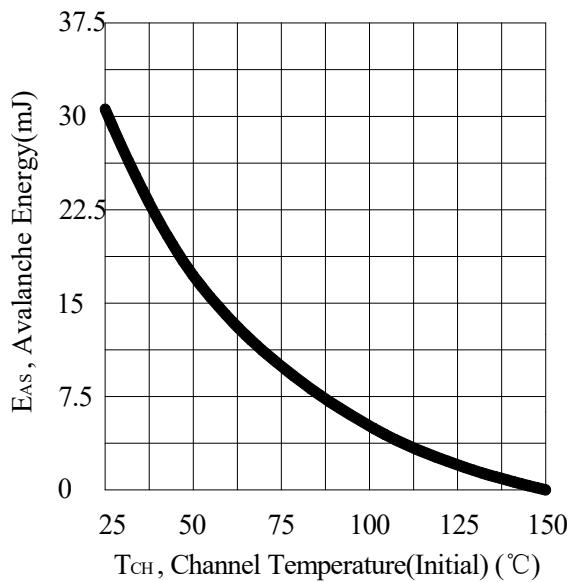
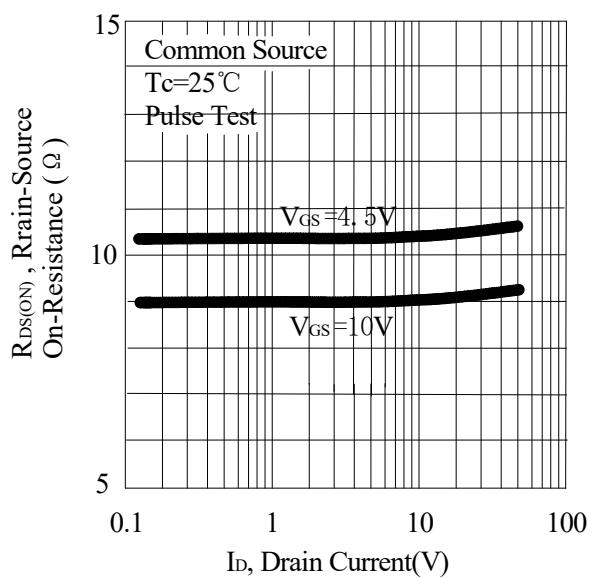
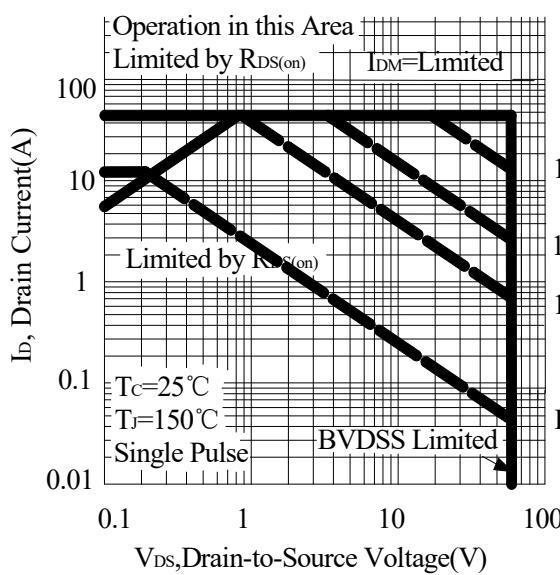
Gate Charge Waveform



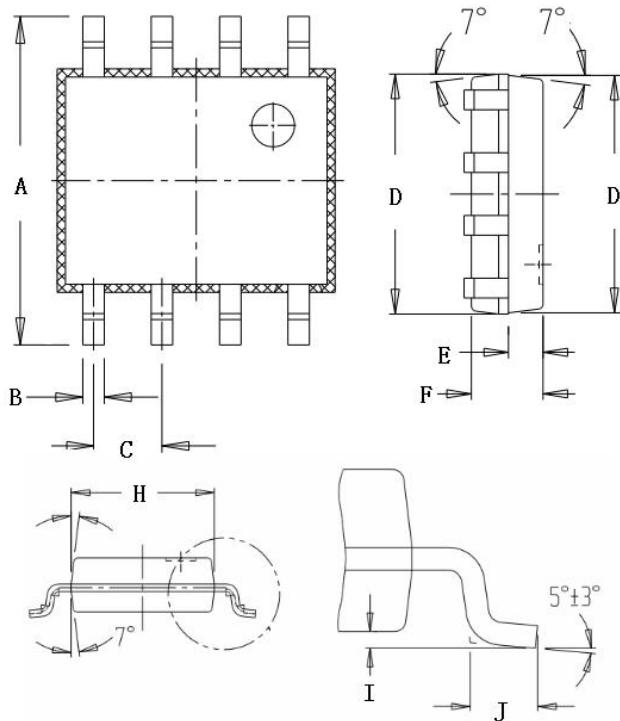
Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES



PACKAGE OUTLINE DIMENSIONS

SOP-8L

SOP8L		
Dim	Min	Max
A	.236(6.0)	.245(6.20)
B	.014(0.37)	.017(0.43)
C	—	.050(1.27)
D	.188(4.80)	.194(4.92)
E	.025(0.65)	.030(0.75)
F	.055(1.40)	.060(1.50)
H	.149(3.80)	.154(3.90)
I	.003(0.10)	.008(0.20)
J	.019(0.50)	.028 (0.70)

Dimensions in inches and (millimeters)

包装方式：编带