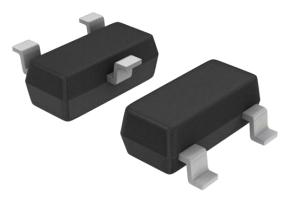


#### Features

- 350 Watts peak pulse power (tp =  $8/20\mu$ s)
- Unidirectional configurations
- Solid-state silicon-avalanche technology
- Low clamping voltage
- Low leakage current
- IEC 61000-4-2 ±8kV contact ±15kV air
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 6A (8/20µs)



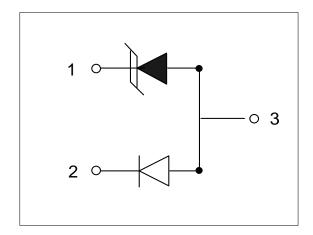
#### Applications

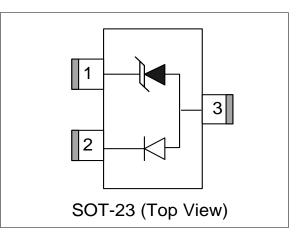
- Dataline
- Automatic Teller Machines
- Net works
- Power line

#### **Mechanical Data**

- SOT-23 package
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel
- RoHS/WEEE Compliant

#### Schematic & PIN Configuration





## **Absolute Maximum Rating**

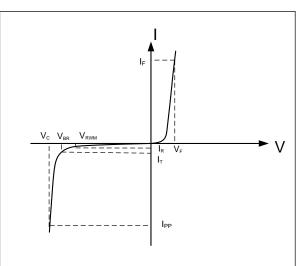
Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20 \mu s$ )	P <sub>PP</sub>	350	Watts
Peak Pulse Current ( $t_p = 8/20\mu s$ ) (note1)	I <sub>pp</sub>	6	А
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V <sub>ESD</sub>	15 8	kV
Lead Soldering Temperature	$T_L$	260(10seconds)	°C
Junction Temperature	TJ	-55 to + 125	°C
Storage Temperature	T <sub>stg</sub>	-55 to + 125	°C

## **Electrical Characteristics**

Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V <sub>RWM</sub>				24	v
Reverse Breakdown Voltage	V <sub>BR</sub>	I <sub>T</sub> =1mA	26.7			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =24V,T=25°C		0.1	0.5	μΑ
Peak Pulse Current	$I_{PP}$	tp =8/20µs			6	А
Clamping Voltage	V <sub>C</sub>	I <sub>PP</sub> =6A,t <sub>p</sub> =8/20µs		50	60	V
Junction Capacitance	C <sub>j</sub>	$V_R = 0V, f = 1MHz$		0.5		pF

# Electrical Parameters (TA = 25 °C unless otherwise noted)

Symbol	Parameter	
Ipp	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
VRWM	Working Peak Reverse Voltage	
IR	Maximum Reverse Leakage Current @ VRWM	
VBR	Breakdown Voltage @ IT	
Іт	Test Current	



Note:.  $8/20\mu s$  pulse waveform.

# **Typical Characteristics**

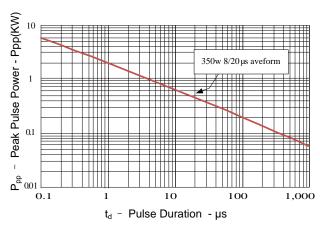


Figure 1: Peak Pulse Power vs. Pulse Time

Percent of Rated Power for Ipp <sup>0</sup> 0 └ Ambient Temperature - T<sub>A</sub> (℃)

Figure 2: Power Derating Curve

Figure3: Pulse Waveform

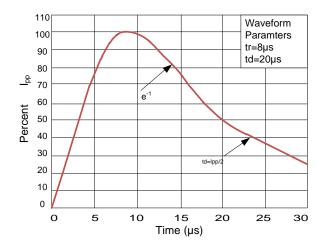
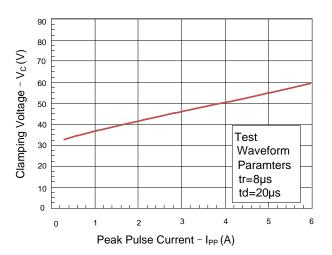


Figure 4: Clamping Voltage vs.lpp

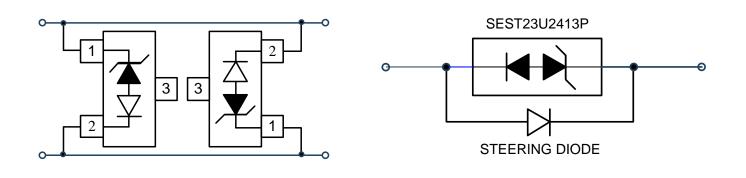


### **Application Information**

The SEST23U2413P series devices are designed to protect high speed data lines. The SEST23U2413P utilizes a low capacitance compensation diode in series with, but in opposite polarity to a TVS diode in each line to achieve an effective capacitance of less than 0.5pF per device. During a transient event, the internal rectifier must be forward biased(TVS is reversed biased). Therefore, each device will only suppress transient events in one polarity. To achieve protection in both positive and negative polarity, a second device is connected in anti-parallel to the first. On unidirectional lines, a fast switching steering diode may be used as an alternative to using two SEST23U2413P devices.

Protection of one unidirectional or bidirectional high-speed line is achieved by connecting two devices in anti-parallel. Pin1 of the first device is connected to the line and pin 2 is connected to ground (or to a second line for differential protection).Pin 2 of the second device is connected to line 1 and pin 1 is connected to ground (or line 2) as shown. Pin 3 is not connected.

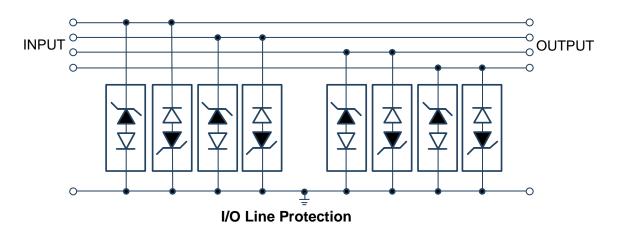
An alternative solution to protect unidirectional lines, is to connect a fast switching steering diode in parallel with the SEST23UXX13P series device. When the steering diode is forward-biased, the TVS will avalanche and conduct in reverse direction. It is important to note that by adding a steering diode, the effective capacitance in the circuit will be increased, therefore the impact of adding a steering diode must be taken in consideration to establish whether the incremental capacitance will affect the circuit functionality or not.



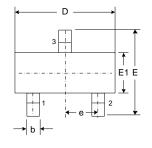
#### **Two Devices: Bidirectional or Unidirectional**

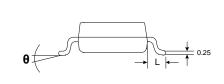


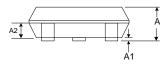
Another typical application, in which the SEST23U2413P series device can be utilized, is to protect multiple I/O lines. The protection in each of the I/O lines is achieved by connecting two devices inverse-parallel



# Outline Drawing – SOT-23

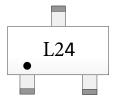






DIMENSIONS					
SYMBOL	MILLIMETER		INCHES		
	MIN	MAX	MIN	MAX	
А	0.89	1.13	0.035	0.044	
A1	0.015	0.11	0.0006	0.0043	
A2	0.60	0.70	0.0236	0.0275	
D	2.72	3.12	0.1070	0.1228	
E	2.60	3.00	0.1024	0.118	
E1	1.40	1.80	0.0551	0.0709	
е	0.95 BSC		0.0374 BSC		
L	0.30	0.60	0.0118	0.0236	
θ	0	8.	0	8.	

# Marking



## **Ordering information**

Order code	Package	Base qty	Delivery mode
SEST23U2413P	SOT-23	3k	Tape and reel